Home (http://ipindia.nic.in/index.htm) About Us (http://ipindia.nic.in/about-us.htm) Who's Who (http://ipindia.nic.in/whos-who-page.htm)
Policy & Programs (http://ipindia.nic.in/policy-pages.htm) Achievements (http://ipindia.nic.in/achievements-page.htm)
RTI (http://ipindia.nic.in/right-to-information.htm) Feedback (https://ipindiaonline.gov.in/feedback) Sitemap (shttp://ipindia.nic.in/itemap.htm)
Contact Us (http://ipindia.nic.in/contact-us.htm) Help Line (http://ipindia.nic.in/helpline-page.htm)



(http://ipindia.nic.in/index.htm)



Patent Search

Invention Title	DESIGN AND ANALYSIS OF ROBUST IOT ARCHITECTURE
Publication Number	18/2023
Publication Date	05/05/2023
Publication Type	INA
Application Number	202341025901
Application Filing Date	06/04/2023
Priority Number	
Priority Country	
Priority Date	
Field Of Invention	COMPUTER SCIENCE
Classification (IPC)	B60W 500000, G06F 303980, G06T 070000, G06T 073500, G07C 050800

Inventor

Name	Address	Country
MR. N V KRISHNA RAO	ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, SCHOOL OF ENGINEERING, MALLA REDDY UNIVERSITY, MAISAMMAGUDA, DULAPALLY, KOMPALLY, HYDERABAD, TELANGANA 500100.	India
MS.V DIVYAVANI	ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, LNSTLTUT OF AERONAUTICAL ENGINEERING, DUNDIGAL, HYDERABAD, TELANGANA 500043.	India
DR. R. OBULAKONDA REDDY	PROFESSOR AND HEAD, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING (CYBER SECURITY), INSTITUTE OF AERONAUTICAL ENGINEERING, DUNDIGAL, HYDERABAD, TELANGANA, INDIA - 500043.	India
DR. M. ARUNA SAFALI	ASSOCIATE PROFESSOR, DEPT. OF CSE, DHANEKULA INSTITUTE OF ENGINEERING AND TECHNOLOGY, VIJAYAWADA.	India
MRS.V.PREMA TULASI	ASSISTANT PROFESSOR, DEPANMENT OF COMPUTER SCIENCE & ENGINEERING, MALLA REDDY INSTITUTE OF ENGINEERING & TECHNOLOGY (AUTONOMOUS), MAISAMMAGUDA, HYDERABAD, TELANGANA, INDIA - 500043.	India

Applicant

Name	Address	Country
MR. N V KRISHNA RAO	ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, SCHOOL OF ENGINEERING, MALLA REDDY UNIVERSITY, MAISAMMAGUDA, DULAPALLY, KOMPALLY, HYDERABAD, TELANGANA 500100.	India
MS.V DIVYAVANI	ASSISTANT PROFESSOR, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING, LNSTLTUT OF AERONAUTICAL ENGINEERING, DUNDIGAL, HYDERABAD, TELANGANA 500043.	India
DR. R. OBULAKONDA REDDY	PROFESSOR AND HEAD, DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING (CYBER SECURITY), INSTITUTE OF AERONAUTICAL ENGINEERING, DUNDIGAL, HYDERABAD, TELANGANA, INDIA - 500043.	India
DR. M. ARUNA SAFALI	ASSOCIATE PROFESSOR, DEPT. OF CSE, DHANEKULA INSTITUTE OF ENGINEERING AND TECHNOLOGY, VIJAYAWADA.	India
MRS.V.PREMA TULASI	ASSISTANT PROFESSOR, DEPANMENT OF COMPUTER SCIENCE & ENGINEERING, MALLA REDDY INSTITUTE OF ENGINEERING & TECHNOLOGY (AUTONOMOUS), MAISAMMAGUDA, HYDERABAD, TELANGANA, INDIA - 500043.	India

Abstract:

Abstract The ToT technology is growing quickly and spreads its branches in almost all areas. It is providing vast employment directly and indirectly in many sectors. In intelligence is incorporated by interconnecting electronic devices like microcontroller, DMA controller, CC cameras, .memory storage devices and various electronic ch transistors. The intelligence applies it action or force as result on end user devices. These devices are non-electrical devices. The non-electrical devices like actuators, generators, inverters, pistons and many other devices are controlled to satisfy user requirements and meet the user demands. The reliability of the system interfacing improved with different supervised algorithms like genetic algorithm, Swarm Optimization algorithm.

I/We Claim 1. To design a loT architecture. 2. The design technique was used to improve the loT process. 3. The proposed design model is used for different applications. 4. The design model is used less power. 5. The suggested approach makes use of all sector.

View Application Status



Terms & conditions (http://ipindia.gov.in/terms-conditions.htm) Privacy Policy (http://ipindia.gov.in/privacy-policy.htm)

Copyright (http://ipindia.gov.in/copyright.htm) Hyperlinking Policy (http://ipindia.gov.in/hyperlinking-policy.htm)

Accessibility (http://ipindia.gov.in/accessibility.htm) Archive (http://ipindia.gov.in/archive.htm) Contact Us (http://ipindia.gov.in/contact-us.htm)

Help (http://ipindia.gov.in/help.htm)

Content Owned, updated and maintained by Intellectual Property India, All Rights Reserved.

Page last updated on: 26/06/2019