

Application Details

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DATE OF FILING	21/10/2019
APPLICANT NAME	1 . INSTITUTE OF AERONAUTICAL ENGINEERING 2 . Dr. VALLABHUNI VIJAY
TITLE OF INVENTION	SYSTEM TO OBTAIN FINITE GAIN AND NOISE OF AN ELECTROCARDIOGRAM AMPLIFIER
FIELD OF INVENTION	BIO-MEDICAL ENGINEERING
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PUBLICATION DATE (U/S 11A)	25/10/2019

Application Status

APPLICATION STATUS	Application Awaiting Examination
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Patent Search

Invention Title	SYSTEM TO OBTAIN FINITE GAIN AND NOISE OF AN ELECTROCARDIOGRAM AMPLIFIER
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Priority Country	
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Field Of Invention	ELECTRONICS
Classification (IPC)	H03F3/38

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Abstract:

"SYSTEM TO OBTAIN FINITE GAIN AND NOISE OF AN ELECTROCARDIOGRAM AMPLIFIER" Exemplary embodiments of the present disclosure are directed towards a system for a system to obtain finite gain and noise of an electrocardiogram (ECG) amplifier, comprising: a computing device may include an electrocardiogram simulator configured to analyze the performance of at least one operational trans-conductance amplifier by simulating using cadence virtuoso analog design environment at 0.18 μm, 0.5 μm and 0.9 μm CMOS technology nodes, wherein the electrocardiogram simulator configured to provide an improved performance at 0.18 μm CMOS technology. FIG. 1

Complete Specification

Claims:What is claimed is:

1. A system to obtain finite gain and noise of an electrocardiogram (ECG) amplifier, comprising: a computing device may include an electrocardiogram simulator configured to analyze the performance of at least one operational trans-conductance amplifier by simulating using cadence virtuoso analog design environment at 0.18 μm, 0.5 μm and 0.9 μm CMOS technology nodes, wherein the electrocardiogram simulator configured to provide an improved performance at 0.18 μm CMOS technology.
2. The system of claim 1, wherein the electrocardiogram simulator comprises at least comb-c filter.
3. The system of claim 2, wherein the at least comb-c filter constructed with the at least one operational trans-conductance amplifier and at least one capacitor that makes it suitable for implementation of monolithic integrated circuits (ICs).
4. The system of claim 2, wherein the comb filter is configured to eliminate undesired signals of fundamental frequency of 60 Hz in ECG signal.
5. The system of claim 1, wherein the electrocardiogram simulator increases the width of at least one transistor and at least one channel length at higher technology nodes to increase passage of current through the transistor for overall gain and phase of at least one operational trans-conductance amplifier. . Description:TECHNICAL FIELD

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FORM 1
THE PATENTS ACT, 1970
(39 of 1970)
&
THE PATENTS RULES, 2003
APPLICATION FOR GRANT OF PATENT
[See sections 7,54 & 135 and rule 20(1)]

(FOR OFFICE USE ONLY)

Application No.:
Filing Date:
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3. TITLE OF THE INVENTION: SYSTEM TO OBTAIN FINITE GAIN AND NOISE OF AN ELECTROCARDIOGRAM AMPLIFIER

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5. PRIORITY PARTICULARS OF THE APPLICATION(S) FILED IN CONVENTION COUNTRY:

Sr.No.	Country	Application Number	Filing Date	Name of the Applicant	Title of the Invention
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6. PARTICULARS FOR FILING PATENT COOPERATION TREATY (PCT) NATIONAL PHASE APPLICATION:

International Application Number	International Filing Date as Allotted by the Receiving Office
PCT//	

7. PARTICULARS FOR FILING DIVISIONAL APPLICATION

Original (first) Application Number	Date of Filing of Original (first) Application

8. PARTICULARS FOR FILING PATENT OF ADDITION:

Main Application / Patent Number:	Date of Filing of Main Application

9. DECLARATIONS:

(i) Declaration by the inventor(s)

I/We ,Dr. VALLABHUNI VIJAY,C V SAIKUMARREDDY ,CHANDRASHAKER PITTALA, P ASHOK BABU, is/are the true & first inventor(s) for this invention and declare that the applicant(s) herein is/are my/our assignee or legal representative.

(a) Date: -----

(b) Signature(s) of the inventor(s):

[Handwritten signatures: Vijay, SAIKUMARREDDY, Chandrasher, Pittala]

(c) Name(s): Dr. VALLABHUNI VIJAY,C V SAIKUMARREDDY ,CHANDRASHAKER PITTALA, P ASHOK BABU

(ii) Declaration by the applicant(s) in the convention country

I/We, the applicant(s) in the convention country declare that the applicant(s) herein is/are my/our assignee or legal representative.

(a) Date: -----

(b) Signature(s) :

(c) Name(s) of the singnatory: INSTITUTE OF AERONAUTICAL ENGINEERING,Dr. VALLABHUNI VIJAY

(iii) Declaration by the applicant(s)


- The Complete specification relationg to the invention is filed with this application.
- I am/We are, in the possession of the above mentioned invention.
- There is no lawful ground of objection to the grant of the Patent to me/us.

10. FOLLOWING ARE THE ATTACHMENTS WITH THE APPLICATION:

Sr.	Document Description	FileName
1	REQUEST FOR EARLY PUBLICATION(FORM-9)	Form 9.pdf
2	REQUEST FOR EXAMINATION (FORM-18)	Form 18.pdf
3	FORM 1	Form 1.pdf
4	COMPLETE SPECIFICATION	Form 2.pdf
5	DRAWINGS	Drawings.pdf
6	STATEMENT OF UNDERTAKING (FORM 3)	Form 3.pdf
7	POWER OF AUTHORITY	POA.pdf
8	DECLARATION OF INVENTORSHIP (FORM 5)	Form 5.pdf

I/We hereby declare that to the best of my/our knowledge, information and belief the fact and matters stated hereing are correct and I/We request that a patent may be granted to me/us for the said invention.

Dated this(Final Payment Date): 21st October, 2019

Signature: 

Name: PUTTA GANESH
(IN/PA/2933)

To The Controller of Patents
The Patent office at CHENNAI