

## **INSTITUTE OF AERONAUTICAL ENGINEERING**

(Autonomous) Dundigal, Hyderabad - 500 043 Department of Electrical and Electronics Engineering

Attainment of Program Outcomes (POs) and Program Specific Outcomes (PSOs) of 2018 - 2022 batch (IARE - R18)

Subject Code	Course Title	P01	P02	P03	P04	P05	P06	P07	P08	P09	P010	P011	P012	PS01	PSO2	PSO3
AHSB02	Linear Algebra and Calculus	1.50	1.30													
AHSB03	Engineering Chemistry	0.80	0.90					1.00								
AHSB08	English Language and Communication Skills Laboratory									2.70	2.70					
AHSB09	Engineering Chemistry Laboratory	2.40	2.40													
AHSB11	Mathematical Transform Techniques	1.90	1.40		2.90									2.10		
AEEB03	Electrical Circuits	1.30	1.30		1.30										1.20	
AHSB04	Waves and Optics	2.00	2.10		2.60									2.90		
AEEB07	Electrical Circuits Laboratory	2.30	2.30	2.30	2.30	2.30	2.30		2.30	2.30	2.30		2.30	2.30		
AEEB09	NETWORK ANALYSIS	1.80	1.70	1.40										2.50		
AEEB10	ELECTROMAGNETIC FIELDS	1.50	1.50	1.40	1.30						1.50		1.30	1.50		
AECB02	ANALOG ELECTRONICS	0.80	0.70	1.00		0.70					0.80			0.70		

AEEB11	ELECTRICAL MACHINES-I	1.50	1.50	1.80										1.80		
AEEB12	NETWORK ANALYSIS LABORATORY	2.00	2.00			2.00			2.00	2.00	2.00		2.00	2.00		2.00
AECB04	ANALOG AND DIGITAL ELECTRONICS LABORATORY	3.00	3.00								3.00			3.00		
AEEB13	ELECTRICAL MACHINES LABORATORY - I	2.30	2.30	2.30	2.30	2.30	2.30		2.30	2.30	2.30		2.30	2.30		
AEEB14	ELECTRICAL POWER GENERATION SYSTEMS	2.20	2.00											2.20		
AEEB15	ELECTRICAL MACHINES - II	2.10	1.90	2.60										1.80		
AEEB16	CONTROL SYSTEMS	1.70	1.80	1.70	1.80		2.00		1.40		1.60		1.60	1.60	1.40	1.90
AEEB18	CONTROL SYSTEMS LABORATORY	1.60	1.60	1.60	1.60	1.60	1.60		1.60	1.60	1.60		1.60	1.60	1.60	1.60
AEEB19	Electrical Power Transmission Systems	1.90	1.90	1.60							1.90			1.90		
AEEB20	Power Electronics	2.00	1.90	1.00	1.20		2.90			2.00	2.00		1.90	2.00	1.60	
AEEB46	Wind and Solar Energy Systems	2.30	2.50	2.30				2.90						1.30	2.40	
AHSB15	Project Based Learning (Prototype / Design Building)	3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00	3.00
AEEB21	Power Electronics Laboratory	2.30	2.30	2.30	2.30	2.30	2.30		2.30	2.30	2.30		2.30	2.30	2.30	2.30
AEEB22	Power System Analysis	2.00	2.00	2.10	2.00					2.00	2.00		2.00	2.00		2.00
AEEB23	Electric Drives and Static Control	1.70	1.70	1.70	1.70		1.70		1.70			1.60	1.80		1.60	

AEEB24	Electrical Measurements and Instrumentation	2.10	2.10	2.40							2.10			2.10		
AEEB45	Power Electronics in Renewable Energy Sources	2.30	2.40	2.50	2.00			2.10						2.20	2.40	
AEEB51	Utilization of Electric Power	2.20	2.10	1.60		0.40									2.80	
AHSB16	Research Based Learning (Fabrication / Model Development)	2.50	2.50	2.50	2.50	2.50	2.50	2.50	2.50	2.50	2.50	2.50	2.50	2.50	2.50	2.50
AEEB25	Electrical Measurements and Instrumentation Laboratory	2.30	2.30	2.30	2.30	2.30			2.30	2.30	2.30		2.30	2.30		2.30
AEEB26	PLC and Industrial Automation Laboratory	2.30	2.30	2.30	2.30	2.30	2.30		2.30	2.30	2.30		2.30	2.30	2.30	2.30
AEEB58	Industrial Automation and Control	2.00	2.20	2.20	1.90		1.90	2.00		1.90	1.90		2.00		2.40	2.00
AEEB27	Power System Protection	1.30	1.30	1.70	1.30	1.20	1.30		1.20	1.30	1.30		1.20	1.30		
AEEB28	Power System Operation and Control	1.20	1.30	1.20	1.30		1.20	1.40		1.20	1.20		1.20	1.30	1.30	
AEEB41	Electrical Safety and Safety Management	1.30	1.30				1.30									
AEEB47	High Voltage Engineering	2.00	2.00	1.90			2.00		2.20		1.80		2.60	2.20	1.80	
AEEB43	HVDC Transmission	2.20	2.20	1.60							2.00			2.00		
AEEB49	Power Quality and FACTS	1.80	2.10	1.80		1.20									2.00	1.50
AEEB29	Electrical Power Systems Laboratory	2.00	2.00	2.00	2.00		2.00		2.00	2.00	2.00		2.00	2.00		2.00
AEEB30	Power System Simulation Laboratory	2.30		2.30		2.30										2.30

AEEB61	Project Work - (phase - I)	2.70	2.70	2.70	2.70	2.70	2.70	2.70	2.70	2.70	2.70	2.70	2.70	2.70	2.70	2.70
AHSB22	Intellectual Property Rights	1.60					1.20		1.30		1.40		1.50			
AEEB54	Electrical and Hybrid Vehicles	2.40	2.30	1.70		1.70		2.60					2.20	2.10	2.90	1.60
	Direct Attainment	2	2	2	1.9	2	2.2	2.1	2.1	2	2.5	2	2.1	2.1	2.1	2

## **Overall Attainment**

<b>GN</b>						]	Progra	m Outo	omes						PSOs	
S No.	Assessment Component(Direct + Indirect)	PO1	PO2	PO3	PO4	PO5	PO6	<b>PO7</b>	PO8	PO9	PO10	PO11	PO12	PSO1	PSO2	PSO3
1.	Direct Assessment (CIA + SEE + Course End Survey) (a)	2.3	2.4	2.4	2.3	2.7	2.6	2.6	2.4	2.8	2.5	2.8	2.7	2.5	2.5	2.3
2.	Student Program exit surveys (b)	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6
3.	Employer surveys (c)	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6	2.6
4.	Alumni Survey (d)	2.6	2.5	2.6	2.5	2.4	2.5	2.5	2.5	2.6	2.5	2.5	2.4	2.6	2.6	2.6
	Overall attainment = a*0.8 + b*0.1 + c*0.05 + d*0.05	2.4	2.4	2.4	2.4	2.7	2.6	2.6	2.4	2.8	2.5	2.8	2.7	2.5	2.5	2.4

## Action taken to improve the attainment of Pos and PSOs:

POs	Target Level	Attainment Level	Observations
PO1: Enginee	ering Knowledge: Appl	y the knowledge of mathematics, scienc	e, engineering fundamentals, and an engineering specialization to the solution of complex engineering problems.
PO1	2.1	2.3	Target is achieved. The following actions were taken to enhance the target level.
Action 2: A few utilization and f	w new FPGA hardware functions of the FPGA k	kits have been purchased and their uses its.	students to solve complex engineering problems in electronics and communication subjects. s have been demonstrated during the theory classes of digital system design course to help the students understand the we been arranged for the students in order to reinforce their knowledge about the application of the subject.
	n Analysis: Identify, fond ngineering sciences.	ormulate, research literature, and analy	ze complex engineering problems reaching substantiated conclusions using first principles of mathematics, natural
PO2	2.1	2.4	Target is achieved. The following actions were taken to enhance the target level.
Action 2: Appl	ication oriented problem	ns were solved in electronic circuit analy	heory and transmission lines and antennas and wave propagation course to analyze and review the research papers. ysis, VLSI design, microprocessor and microcontroller and embedded systems to improve logical thinking. rere taken for the application of theory to real life problem.
		<b>ns:</b> Design solutions for complex engine e cultural, societal, and environmental c	eering problems and design system components or processes that meet the specified needs with appropriate consideration onsiderations.
PO3	2.1	2.4	Target is achieved. The following actions were taken to enhance the target level.
Action 2: In ele	ectronic devices and circ		y to students in open elective courses. assignments were given to students to solve real field design problems. the students in electronic devices and circuit and control systems courses.
	Investigations of Com provide valid conclusion		owledge and research methods including design of experiments, analysis and interpretation of data, and synthesis of the
PO4	2.1	2.3	Target is achieved. The following actions were taken to enhance the target level.

Action 1: Several workshops were conducted on the design of electronic devices and circuits, embedded system and microprocessor & microcontroller lab experiments. Action 2: In VLSI design, special attention was given to demonstrate how to analyze and interpret experimental data and synthesize a research conclusion/outcome. Action 3: Research based complex problems were given as assignment in VLSI design, signals & system, digital communication, to improve practical skills of the students

**PO5: Modern Tool usage:** Create, select, and apply appropriate techniques, resources, and modern engineering and IT tools including prediction and modelling to complex engineering activities with an understanding of the limitations.

PO5	2.1	2.7	Target is achieved. The following actions were taken to enhance the target level.
Action 1: Com	plex design problem usi	ng modern tool were given as assignme	nt to the students in digital signal processing, digital image processing and embedded system design courses.
Action 2: Work	shops were conducted	on several electronics and communication	on engineering software packages like PCB Design, antenna design(HFSS) and Cadence.
Action 3: Han processing cour		TLAB-simulink engineering tool were	e given to students to understand the concept of analog communication, digital communication and digital signal
0	ineer and Society: App gineering practice.	ly reasoning informed by the contextual	knowledge to assess societal, health, safety, legal and cultural issues and the consequent responsibilities relevant to the
PO6	2.1	2.6	Target is achieved. The following actions were taken to enhance the target level.
Action 2: Stude	ents will be encouraged	to participate in social clubs like sports	o inculcate a strong sense of responsibility among the budding student engineers. club, cultural club
sustainable dev			
PO7	2.1	2.6	Target is achieved. The following actions were taken to enhance the target level.
Action 2: Awar	reness program on effec	t of electronics circuit to nature will be	ong the students and also to promote sustainable environment. given to promote a sustainable environment. energy projects using optimized material that would guarantee sustainable development.
PO8: Ethics: A	apply ethical principles a	and commit to professional ethics and re	sponsibilities and norms of the engineering practice.
PO8	2.1	2.4	Target is achieved. The following actions were taken to enhance the target level.

Action 2: Onlin	ne video links on ethical	on topics related to professional ethics principles in electronics subjects were s get their major project and internship re	
PO9 : Individı	ual and Team Work: Fu	unction effectively as an individual, and	as a member or leader in diverse teams, and in multidisciplinary settings.
PO9	2.1	2.8	Target is achieved. The following actions were taken to enhance the target level.
Action 2: Seve	ral students' professiona		here they will learn to function effectively both as individuals and as team members in a group. emonstrate their abilities as team members in a group. p.
		e effectively on complex engineering act e effective presentations, and give and r	tivities with the engineering community and with society at large, such as, being able to comprehend and write effective eceive clear instructions.
PO10	2.1	2.5	Target is achieved. The following actions were taken to enhance the target level.
Action 2: Stude	ents were encouraged to		ctive presentations on projects undertaken. and national/international conferences/seminars/symposia/ hackathon / ideathon. s/public speaking of the students.
		ance: Demonstrate knowledge and unde disciplinary environments.	rstanding of the engineering and management principles and apply these to one's own work, as a member and leader in
PO11	2.1	2.8	Target is achieved. The following actions were taken to enhance the target level.
		do multidisciplinary project involving a andle financial management during maj	
PO12:Life-lon	ng Learning: Recognize	the need for, and have the preparation a	and ability to engage in independent and life-long learning in the broadest context of technological change.
PO12	2.1	2.7	Target is achieved. The following actions were taken to enhance the target level.

Action 2: Stude	ents were motivated to ta	ake up NPTEL certification on python,	unication, cyber security, machine learning, artificial intelligence, will be introduced to the students. networking, RF design, communication, VLSI related subjects. ess communication, RF design, semiconductor technology in premier institution.
<b>PSO1:</b> Build en	nbedded software and di	gital circuit development platform for ro	botics, embedded systems and signal processing applications.
PSO1	2.1	2.5	Target is achieved. The following actions were taken to enhance the target level.
Action 2: Stud	ents were given design		eir project work so that they can design, analyze and find solution which gives exposure to latest technologies. of electronics and communication engineering curricular activities.
<b>PSO2:</b> Focus o	n the application specifi	c integrated circuit prototype designs, v	rtual instrumentation and system on chip designs.
PSO2	2.1	2.5	Target is achieved. The following actions were taken to enhance the target level.
Action 1: Hand Action 2: Shor	ds on workshop were co t term training program		hardware and software for getting real time exposure.
Action 1: Hand Action 2: Shor Action 3: Stude	ds on workshop were co t term training program ents were motivated to ta	nducted from industry experts on latest were conducted on program specific co ake up industry related project to get un	hardware and software for getting real time exposure.
Action 1: Hand Action 2: Shor Action 3: Stude	ds on workshop were co t term training program ents were motivated to ta	nducted from industry experts on latest were conducted on program specific co ake up industry related project to get un	hardware and software for getting real time exposure. arses derstanding of advanced industry tools.





