

**MECHANICAL ENGINEERING****ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT**

Name of the faculty: **Ms. B VEENA** Department: **Mechanical Engineering**
 Regulation: **IARE - R18** Batch: **2019-2023**
 Course Name: **Microprocessors and Interfacing** Course Code: **AECB55**
 Semester: **VIII** Target Value: **60% (1.8)**

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Outline the functional components of microprocessors and microcontrollers for understanding the operation of architectures	0.90	2.20	1.2	Not Attained
CO2	Make use of addressing modes and instruction set of target microprocessors and microcontrollers for writing an assembly language programs to perform a task.	1.30	2.20	1.5	Not Attained
CO3	Demonstrate the internal architecture and modes of operation of peripheral devices for interfacing memory and I/O devices.	0.90	2.20	1.2	Not Attained
CO4	Illustrate the interrupt handling mechanism in microprocessors and microcontrollers using interrupt controller.	0.90	2.20	1.2	Not Attained
CO5	Choose an appropriate data transfer scheme and hardware for data transfer between the devices	2.30	2.20	2.3	Attained
CO6	Develop microprocessor and microcontroller based applications using necessary input and output devices.	0.90	2.20	1.2	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: More assignments may be given on the functional components of microprocessors and microcontrollers.

CO2: More assignments may be given on writing an assembly language programs to perform a task.

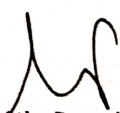
CO3: More assignments may be given on the internal architecture and modes of operation of peripheral devices for interfacing memory and devices.

CO4: More example may be given on the use of interrupt controller.

CO6: More case studies may be given on development of microprocessor and microcontroller based applications using necessary input and output devices.


Course Coordinator


Mentor


Head of the Department
Head of the Department
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