



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

INFORMATION TECHNOLOGY


ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Dr. B POLAIAH	Department:	Information Technology
Regulation:	IARE - BT23	Batch:	2023-2027
Course Name:	Computer System Architecture	Course Code:	AECD04
Semester:	III	Target Value:	60% (1.8)


Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Demonstrate a thorough understanding of the basic concepts and principles of computer system architecture.	3.00	2.20	2.8	Attained
CO2	Analyze different types of instruction sets and addressing modes.	2.40	2.20	2.4	Attained
CO3	Evaluate memory management techniques such as paging, segmentation and virtual memory.	2.40	2.20	2.4	Attained
CO4	Compare different I/O techniques, including programmed I/O, interrupt driven I/O, and direct memory access (DMA).	3.00	2.20	2.8	Attained
CO5	Explore the implications of parallel processing and apply concepts of pipelining and parallelism to enhance system performance.	2.40	2.20	2.4	Attained
CO6	Summarize the concepts of pipelining and interprocess communication for advanced processor design.	2.40	2.20	2.4	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)


Course Coordinator


Mentor


Head of the Department