INSTITUTE OF AERONAUTICAL ENGINEERING
(Autonomous)
Dundigal, Hyderabad - 500043, Telangana

INFORMATION TECHNOLOGY

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	**Ms.T SARITHA KUMARI**	Department:	**Information Technology**
Regulation:	**IARE - R20**	Batch:	**2022-2026**
Course Name:	**Basic Electrical Engineering**	Course Code:	**AEEC01**
Semester:	**I**	Target Value:	**60% (1.8)**

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Solve complex electrical circuits by applying network reduction techniques for reducing into a simplified circuit.	0.90	2.30	1.2	Not Attained
CO2	Define basic nomenclature of single phase AC circuits for obtaining impedance, admittance of series and parallel circuits.	1.60	2.20	1.7	Not Attained
CO3	Make use of various network theorems and graph theory for simplifying complex electrical networks.	0.90	2.20	1.2	Not Attained
CO4	Demonstrate the construction, principle and working of DC machines for their performance analysis.	1.60	2.30	1.7	Not Attained
CO5	Illustrate working , construction and obtain the equivalent circuit of single phase transformers.	2.30	2.20	2.3	Attained
CO6	Explore electromagnetic lawsused for the construction and opertaion of synchronous and asynchronous machines.	0.90	2.30	1.2	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: More tutorials can be given on applying network reduction techniques for reducing into a simplified circuit.

CO2: More assignments are to be given on single phase AC circuits for obtaining impedance, admittance of series and parallel circuits.

CO3: More tutorials can be given on various network theorems and graph theory for simplifying complex electrical networks.
CO4: Additional reading materials are provided on the construction, principle and working of DC machines for their performance analysis.
CO6: Digital content and additional reading materials are provided on electromagnetic laws used for the construction and operation of synchronous and asynchronous machines.

Course Coordinator **Mentor** **Head of the Department**