



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

## INFORMATION TECHNOLOGY

### ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Ms. B LAKSHMI PRASANNA	Department:	Information Technology
Regulation:	IARE - R20	Batch:	2022-2026
Course Name:	Analog and Digital Electronics	Course Code:	AECC08
Semester:	III	Target Value:	60% (1.8)

#### Attainment of COs:

Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1 Demonstrate the volt-ampere characteristics of semiconductor devices for finding cut-in voltage, resistance and capacitance.	0.90	2.30	1.2	Not Attained
CO2 Illustrate half wave and full wave rectifier circuits with filter and without filters used to convert the alternating current in to direct current.	0.90	2.30	1.2	Not Attained
CO3 Analyze the input and output characteristics of transistor configurations and small signal h-parameter models to determine the input - output resistances, current gain and voltage gain	1.60	2.30	1.7	Not Attained
CO4 Identify the functionality of logic gates, parity code and hamming code techniques for error detection and correction of single bit in digital systems.	3.00	2.30	2.9	Attained
CO5 Make use of appropriate logic gates to implement combinational logic circuits.	2.30	2.30	2.3	Attained
CO6 Select a required flip flop to realize synchronous and asynchronous counters for memory storing applications.	2.00	2.30	2.1	Attained

#### Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Solving More analytical questions on characteristics of diode

CO2: Practicing derivative part related to rectifiers and solving problems from these formulas.

CO3: explaining transistor configurations and its hybrid model to calculate total input impedance and current gain by using exact hybrid model

Course Coordinator

Mentor

Head of the Department

Information Technology

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