



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

INFORMATION TECHNOLOGY

ATTAINMENT OF COURSE OUTCOME – ACTION TAKEN REPORT

Name of the faculty:	B .LAKSHMI PRASANNA	Department:	IT
Regulation:	IARE - R16	Batch:	2016 - 2020
Course Name:	Microprocessors Interfacing and Applications	Course Code:	AEC023
Semester:	VI	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome		Direct attainment	Indirect attainment	Overall attainment	Observation
CO1	Outline the functional components of microprocessors for understanding the operation of architectures.	0.9	2.6	1.2	Attainment target is not yetreached.
CO2	Make use of addressing modes and instruction set of target microprocessors for writing an assembly language programs to perform a task.	0.9	2.6	1.2	Attainment target is not yetreached.
CO3	Demonstrate the internal architecture and modes of operation of peripheral devices for interfacing memory and I/O devices.	0.9	2.5	1.2	Attainment target is not yetreached.
CO4	Illustrate the interrupt handling mechanism in microprocessors and microcontrollers using interrupt controller.	0.9	2.6	1.2	Attainment target is not yetreached.
CO5	Choose an appropriate data transfer scheme and hardware for data transfer between the devices.	1.6	2.6	1.8	Attainment target reached
CO6	Develop microprocessor based applications using necessary input and output devices.	0	2.6	0.5	Attainment target is not yetreached.

Action taken report: (To be filled by the concerned faculty / course coordinator)

For example: CO1,CO2,CO3,CO4,CO6

Need to provide more examples for handling mechanism in microprocessors and microcontrollers.

Laxmi
Course Coordinator

[Signature]
Mentor

[Signature]
HOD