INSTITUTE OF AERONAUTICAL ENGINEERING
(Autonomous)
Dundigal, Hyderabad - 500043, Telangana

EMBEDDED SYSTEMS

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	**Ms. INDIRA KAMBALA**	Department:	**Embedded Systems**
Regulation:	**IARE - R21**	Batch:	**2022-2024**
Course Name:	**Microcontrollers and Programmable Digital Signal Processing**	Course Code:	**BESC02**
Semester:	**I**	Target Value:	**60% (1.8)**

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Illustrate the Internal architecture and memory operations of ARM Cortex M3 processor for interfacing microprocessor applications	2.10	1.50	2	Attained
CO2	Analyze exceptions handler mechanism to minimize interrupt latency using Nested Vectored Interrupt Controller	3.00	2.10	2.8	Attained
CO3	Construct the high level of integration in embedded applications using LPC 17XX Microcontroller	0.90	1.70	1.1	Not Attained
CO4	Demonstrate various computational building blocks of programmable DSP architectures using interfacing of memory and I/O peripherals	3.00	1.70	2.7	Attained
CO5	Identify the CPU architecture, peripherals, and development tools for the TMS320C6000 digital signal processors	0.90	1.80	1.1	Not Attained
CO6	Develop the application for digital signal processing using code composer studio tool	0.90	1.80	1.1	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO3: Guest lectures will conduct on LPC 17XX Microcontroller and its applications

CO5: Tutorial classes will be conduct on CPU architecture, peripherals, and development tools for the TMS320C6000 digital signal processors

CO6: Guest lectures will conduct on code composer studio tool for real time embedded applications

Course Coordinator

Mentor

Head of the Department

Head of the Department
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