INSTITUTE OF AERONAUTICAL ENGINEERING
(Autonomous)
Dundigal, Hyderabad - 500043, Telangana

EMBEDDED SYSTEMS

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	**Ms. B VEENA**	Department:	**Embedded Systems**
Regulation:	**IARE - R21**	Batch:	**2022-2024**
Course Name:	**Embedded System Design and Architecture**	Course Code:	**BESC01**
Semester:	**I**	Target Value:	**60% (1.8)**

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Demonstrate the concepts of embedded systems and formalisms for System design	2.70	1.40	2.4	Attained
CO2	Apply the suitable memory technology and other components for different applications to meet the ever growing needs of the embedded applications.	2.40	1.70	2.3	Attained
CO3	Choose the fundamental components that make up an embedded board to implement an Instruction Set Architecture's features in a processor	3.00	1.80	2.8	Attained
CO4	Categorize the embedded firmware design approaches and development languages used for programming embedded devices.	0.60	1.70	0.8	Not Attained
CO5	Make use of the memory hierarchy to minimize the access time in embedded architecture design.	0.90	2.00	1.1	Not Attained
CO6	Identify the hardware software co- design issues pertaining to design of an embedded system using low power microcontrollers.	3.00	1.80	2.8	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO4: Tutorials will be conduct on embedded firmware design approaches and development languages

CO5: Assignments will be given on memory hierarchy and tutorials will be conduct on embedded architecture design.

Course Coordinator   **Mentor**   **Head of the Department**

Head of the Department
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