



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Ms C V P Supradeepthi	Department:	M.Tech- EMBEDDED SYSTEMS
Regulation:	PG21	Batch:	2021-2023
Course Name:	Advanced Microprocessor and Interfacing	Course Code:	BECS13
Semester:	II	Target Value:	60% (1.8)

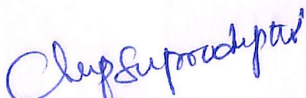
Attainment of COs:

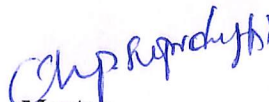
Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Describe the features of ARM processors for signal description and architecture.	2.3	1.2	2.1	Target Attained
CO2	Illustrate the programmer's model of ARM processor and test the programming model using high level and low level languages.	1.6	1.2	1.5	Target not Attained
CO3	Demonstrate the internal architecture and various modes of operation of the devices used for interfacing memory and I/O devices with ARM processor.	0.9	2.4	1.2	Target not Attained
CO4	Apply the memory management architecture for allocating the MMU	3	3.0	3	Target Attained
CO5	Analyze floating point processor architecture and its architectural support for higher level language.	3	1.2	2.6	Target Attained
CO6	Build prototype models and products subsequently in embedded field for real life needs and applications.	1.6	2.4	1.8	Target Attained

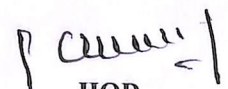
Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO2: Guest lectures will be conducted on Illustrate the programmer's model of ARM processor and test the programming model using high level and low level languages.

CO3: Additional information will be conducted on the internal architecture and various modes of operation of the devices used for interfacing memory and I/O devices with ARM processor


Course Coordinator


Mentor


HOD

Dr. P. MUNASWAMY M.Tech, Ph.D, MISTE
Professor & Head
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