



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)  
Dundigal, Hyderabad - 500 043

## ELECTRONICS AND COMMUNICATION ENGINEERING

### ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Ms. K Indira	Department:	M.Tech- EMBEDDED SYSTEMS
Regulation:	PG-21	Batch:	2021-2023
Course Name:	Microcontrollers and Programmable Digital Signal Processing	Course Code:	BESC02
Semester:	I	Target Value:	60% (1.8)

#### Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Illustrate the Internal architecture and memory operations of ARM Cortex M3 processor for interfacing microprocessor applications	3	1.8	2.8	Target Attained
CO2	Analyze exceptions handler mechanism to minimize interrupt latency using Nested Vectored Interrupt Controller	0.6	1.2	0.7	Target not Attained
CO3	Construct the high level of integration in embedded applications using LPC 17XX Microcontroller	3	1.2	2.6	Target Attained
CO4	Demonstrate various computational building blocks of programmable DSP architectures using interfacing of memory and I/O peripherals	2.1	2.4	2.2	Target Attained
CO5	Identify the CPU architecture, peripherals, and development tools for the TMS320C6000 digital signal processors	1.6	3.0	1.9	Target Attained
CO6	Develop the application for digital signal processing using code composer studio tool	0	1.2	0.2	Target not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO2: Conduct tutorials will be on exceptions handler mechanism to minimize interrupt latency for more practice on real time applications.

CO6: Giving assignments and conduct tutorials will be on digital signal processing using code composer studio tool.

*K. Indira*  
Course Coordinator

*K. Indira*  
Mentor

*P. Munaswamy*  
HOD

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