INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	**Ms. B. Veena**	Department:	**M.Tech- EMBEDDED SYSTEMS**
Regulation:	**PG21**	Batch:	**2021-2023**
Course Name:	**Embedded System Design and Architecture**	Course Code:	**BESC01**
Semester:	**I**	Target Value:	**60% (1.8)**

Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Demonstrate the concepts of embedded systems and formalisms for System design	2.3	1.8	2.2	Target Attained
CO2	Apply the suitable memory technology and other components for different applications to meet the ever growing needs of the embedded applications.	2.7	2.4	2.6	Target Attained
CO3	Choose the fundamental components that make up an embedded board to implement an Instruction Set Architecture's features in a processor	0.9	1.2	1	Target not Attained
CO4	Categorize the embedded firmware design approaches and development languages used for programming embedded devices.	2.7	1.8	2.5	Target Attained
CO5	Make use of the memory hierarchy to minimize the access time in embedded architecture design.	0.9	1.2	1	Target not Attained
CO6	Identify the hardware software co- design issues pertaining to design of an embedded system using low power microcontrollers.	3	3.0	3	Target Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO3: Conduct tutorials will be on Instruction Set Architecture's features in a processor for more practice on real time applications.
CO5: Giving assignments and conduct tutorials will be on embedded architecture design.

B. Veena
Course Coordinator

Mentor

HOD

Dr. P. MUNASWAMY M.Tech, Ph.D, MISTE
Professor & Head
ELECTRONICS AND COMMUNICATION ENGINEERING
INSTITUTE OF AERONAUTICAL ENGINEERING
Dundigal, Hyderabad- 500 043, T.S.