INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Dr. S China Venkateswarlu	Department:	M.TECH-EMBEDDED SYSTEMS
Regulation:	R18	Batch:	2019-2021
Course Name:	Micro Controllers And Programmable Digital Signal Processing	Course Code:	BESB02
Semester:	I	Target Value:	60% (1.8)

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Illustrate the Internal architecture and memory operations of ARM Cortex M3 processor for interfacing microprocessor applications	3	2.5	2.9	Attainment target reached
CO2	Analyze Exceptions handler mechanism to minimize interrupt latency using Nested Vectored Interrupt Controller	1.6	2.3	1.7	Attainment target is not yet reached
CO3	Construct the high level of integration in embedded applications using LPC 17XX Microcontroller	1.6	2.5	1.8	Attainment target reached
CO4	Demonstrate various computational building blocks of programmable DSP architectures using interfacing of memory and I/O peripherals	3	2.3	2.9	Attainment target reached
CO5	Identify the CPU architecture, peripherals, and development tools for the TMS320C6000 digital signal processors	3	2.3	2.9	Attainment target reached
CO6	Develop the application for digital signal processing using code composer studio tool	3		2.4	Attainment target reached

Action Taken Report: (To be filled by the concerned faculty/course coordinator)
CO 3: Additional inputs are provided on the exceptions handling mechanism to minimize interrupt latency.

Course Coordinator

Mentor

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