



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Dr. V Padmanabha Reddy	Department:	M.TECH-EMBEDDED SYSTEMS
Regulation:	R18	Batch:	2018-2020
Course Name:	Embedded System Architecture	Course Code:	BESB11
Semester:	II	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Summarize the fundamental components that make up an embedded board to implement an Instruction Set Architecture's features in a processor.	0.9	1.4	1	Attainment target is not yet reached
CO2	Detect the internal processor design operations to achieve better performance used in embedded systems.	0.9	2.0	1.1	Attainment target is not yet reached
CO3	Apply the suitable hardware and memory technology for different applications to meet the ever growing needs of the embedded applications.	0.9	1.4	1	Attainment is not yet target reached
CO4	Make use an appropriate middleware software for real time embedded system based design .	0.9	1.4	1	Attainment target is not yet reached
CO5	Categorize the different design stages for designing the embedded systems.	0.9	2.0	1.1	Attainment target reached
CO6	Identify the hardware software co- design issues pertaining to design of an embedded system using low power microcontrollers.	0.9	1.6	1	Attainment target is not yet reached

Action Taken Report: (To be filled by the concerned faculty/course coordinator)

CO 1: Additional inputs are provided on the fundamental components that make up an embedded board to implement an Instruction Set Architecture.

CO 2: Giving assignments and conducting tutorial classes on the internal processor design operations to achieve better performance.

CO 3: Additional inputs are provided on memory technology for different applications to meet the ever-growing needs of embedded applications.

CO 4: Conducting Guest lectures on middleware software for real-time embedded system-based design.

CO 5: Additional inputs are provided on the different design stages for designing the embedded systems.

CO 6: Giving assignments and conducting tutorial classes on the hardware-software co-design issues pertaining to the design of an embedded system using low-power microcontrollers.


Course Coordinator


Mentor


HOD

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