



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)  
Dundigal, Hyderabad - 500 043

## ELECTRONICS AND COMMUNICATION ENGINEERING

### ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Dr. S China Venkateswarlu	Department:	M.TECH-EMBEDDED SYSTEMS
Regulation:	R18	Batch:	2018-2020
Course Name:	Micro Controllers And Programmable Digital Signal Processing	Course Code:	BESB02
Semester:	I	Target Value:	60% (1.8)

#### Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Illustrate the Internal architecture and memory operations of ARM Cortex M3 processor for interfacing microprocessor applications	3	1.4	2.7	Attainment target reached
CO2	Analyze Exceptions handler mechanism to minimize interrupt latency using Nested Vectored Interrupt Controller	3	2.0	2.8	Attainment target reached
CO3	Construct the high level of integration in embedded applications using LPC 17XX Microcontroller	0.9	1.4	1	Attainment is not yet target reached
CO4	Demonstrate various computational building blocks of programmable DSP architectures using interfacing of memory and I/O peripherals	2.3	1.4	2.1	Attainment target reached
CO5	Identify the CPU architecture, peripherals, and development tools for the TMS320C6000 digital signal processors	3	2.0	2.8	Attainment target reached
CO6	Develop the application for digital signal processing using code composer studio tool	1.6	1.6	1.6	Attainment target is not yet reached

Action Taken Report: (To be filled by the concerned faculty/course coordinator)

CO 3: Additional inputs are provided on the LPC 17XX Microcontroller and construction of high-level integration.  
CO 6: Conducting Guest lectures on code composer studio tools.

  
Course Coordinator

  
Mentor

  
HOD  
Dr. P. ASHOK BABU, M.E. Ph.D  
Professor & Head  
ELECTRONICS AND COMMUNICATION ENGINEERING  
INSTITUTE OF AERONAUTICAL ENGINEERING  
Dundigal, Hyderabad - 500 043, T.S.