

INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:

Ms. V BINDU SREE

Department:

Electronics and Communication Engineering

Regulation:

IARE - BT23

Batch:

2023-2027

Course Name:

Electrical Circuits Laboratory

Course Code:

AEED04

Semester:

Target Value:

60% (1.8)

Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Calculate the source resistance, currents, voltage and power in an electrical circuit using various laws associated with electrical circuits	0.90	0.00	0.9	Not Attained
CO2	Evaluate the alternating quantities for different periodic waveforms.	0.90	0.00	0.9	Not Attained
CO3	Describe the superposition principle, reciprocity and maximum power transfer condition for the electrical network with DC excitation.	0.90	0.00	0.9	Not Attained
CO4	Demonstrate Thevenin's and Norton's theorems to reduce complex networks into simple equivalent networks with DC excitation.	0.90	0.00	0.9	Not Attained
CO5	Apply Faraday's laws of electromagnetic induction in the construction of magnetic circuits.	0.90	0.00	0.9	Not Attained
CO6	Make use of the two port parameters to be measured easily, without solving for all the internal voltages and currents in the different networks.	0.90	0.00	0.9	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Tutorial classes will be conduct on basics of electrical circuits and more practice is required.

CO2: Tutorial classes will conduct on basics of ohms law ,periodic waveforms for electrical circuits.

CO3: Guest lecture will conduct on superposition theorem, reciprocity theorem of AC and DC circuits.

CO4: Tutorial classes will be conduct on basics of electrical circuits and more practice is required.

CO5: Guest lecture will conduct on faradays laws of electromagnetic induction for magnetic circuits.

CO6: Tutorial classes will conduct on two port parameters of voltage and current for different networks.

ELECTRONICS AND COMMUNICATION ENGINEERING INSTIT Head of the Department EERING

Dundigal, Hyderabad- 500 043. T.S.