



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty: **Mr. BALA THIMMAIAH NAKKA** Department: **Electronics and Communication Engineering**
Regulation: **IARE - BT23** Batch: **2023-2027**
Course Name: **System Design and Modeling** Course Code: **AECD18**
Semester: **IV** Target Value: **60% (1.8)**

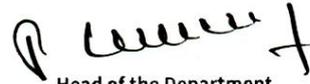
Attainment of COs:

Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1 Understand the fundamental concepts and importance of hardware description languages (HDLs) in digital system design.	3.00	0.00	3	Attained
CO2 Demonstrate proficiency in the syntax and semantics of Verilog/VHDL for designing digital circuits	3.00	0.00	3	Attained
CO3 Design and optimize combinational circuits such as adders, subtractors, multiplexers, and decoders using Boolean expressions and truth tables.	3.00	0.00	3	Attained
CO4 Design sequential circuits, including flip-flops, counters, and registers, for use in complex digital systems.	3.00	0.00	3	Attained
CO5 Verify and debug the functionality of practical digital systems through simulation and testing on hardware platforms like FPGAs or simulators.	3.00	0.00	3	Attained
CO6 Analyze the trade-offs in terms of area, power, and performance for the implemented designs to meet real-world constraints.	3.00	0.00	3	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)


Course Coordinator


Mentor


Head of the Department

Dr. P. MUNASWAMY M.Tech, Ph.D, MISTE
Professor & Head
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