



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

## ELECTRONICS AND COMMUNICATION ENGINEERING

### ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Mr. A SRIKANTH	Department:	Electronics and Communication Engineering
Regulation:	IARE - R20	Batch:	2022-2026
Course Name:	Electrical Circuits Laboratory	Course Code:	AEEEC03
Semester:	II	Target Value:	60% (1.8)

#### Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Calculate the source resistance, current and voltage using Ohm's, Kirchhoff's laws, mesh and nodal analysis techniques in electrical circuits.	3.00	0.00	3	Attained
CO2	Analyze the alternating quantities for different periodic wave forms.	3.00	0.00	3	Attained
CO3	Apply network theorems for complex circuits and verify the current, voltage and power in DC networks.	3.00	0.00	3	Attained
CO4	Validate the resistance, inductance and power consumed by passive loads using digital simulation.	3.00	0.00	3	Attained
CO5	Analyze the concepts of impedance, admittance hybrid and transmission parameters of a two port network.	3.00	0.00	3	Attained
CO6	Determine the resonant frequency, quality factor and bandwidth of the RLC circuits.	3.00	0.00	3	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

  
Course Coordinator

  
Mentor

  
Head of the Department

Head of the Department  
ELECTRONICS AND COMMUNICATION ENGINEERING  
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