



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

## ELECTRONICS AND COMMUNICATION ENGINEERING

### ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty: **Mr. B NARESH** Department: **Electronics and Communication Engineering**  
 Regulation: **IARE - UG20** Batch: **2022-2026**  
 Course Name: **Microprocessors and Microcontrollers** Course Code: **AECC19**  
 Semester: **V** Target Value: **60% (1.8)**

#### Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Describe the features of intel processors and micro controllers for signal description and architecture	3.00	2.10	2.8	Attained
CO2	Make use of addressing modes and instruction set of target microprocessors and micro controllers for writing efficient assembly language programs	2.00	2.00	2	Attained
CO3	Make use of addressing modes and instruction set of target microprocessors and micro controllers for writing efficient assembly language programs	0.90	2.10	1.1	Not Attained
CO4	Illustrate the interrupt handling mechanism in microprocessors and micro controllers using interrupt controller.	2.30	2.10	2.3	Attained
CO5	Choose an appropriate data transfer scheme and hardware for data transfer between the devices.	1.60	2.10	1.7	Not Attained
CO6	Develop microprocessor and micro controller based applications using appropriate input and output devices	2.30	2.10	2.3	Attained

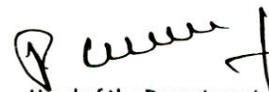
#### Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO3: Guest Lecture will be conduct on use of addressing modes and instruction set of target microprocessors and micro controllers for writing efficient assembly language programs.

CO5: Assignments will be given on data transfer schemes and hardware for data transfer between the devices.

  
Course Coordinator

  
Mentor

  
Head of the Department

**Dr. P. MUNASWAMY** M.Tech, Ph.D, MISTE  
Professor & Head  
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