



## ELECTRONICS AND COMMUNICATION ENGINEERING

### ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	<b>Ms. VEENA M KURUP</b>	Department:	<b>Electronics and Communication Engineering</b>
Regulation:	<b>IARE - R20</b>	Batch:	<b>2022-2026</b>
Course Name:	<b>Digital System Design Laboratory</b>	Course Code:	<b>AECC06</b>
Semester:	<b>III</b>	Target Value:	<b>60% (1.8)</b>

**Attainment of COs:**

	<b>Course Outcome</b>	<b>Direct Attainment</b>	<b>Indirect Attainment</b>	<b>Overall Attainment</b>	<b>Observation</b>
CO1	Utilize the concept of Boolean algebra to verify the truth table of Boolean expressions using logic gates in Hardware Description Language.	1.60	0.00	1.6	Not Attained
CO2	Make use of dataflow, structural and behavioral modelling styles of HDL for simulating the combinational logic circuits.	1.60	0.00	1.6	Not Attained
CO3	Analyze the truth tables and characteristic equations of flip flops for the functional simulation and timing analysis of sequential circuits.	1.60	0.00	1.6	Not Attained
CO4	Construct the synchronous and asynchronous sequential circuits using the flip flops.	1.60	0.00	1.6	Not Attained
CO5	Model a finite state machine with mela and moore machines for detecting a given sequence.	1.60	0.00	1.6	Not Attained
CO6	Examine the functionality of real time traffic light controller, chess clock controller FSM, elevator operations using HDL code.	1.60	0.00	1.6	Not Attained

**Action Taken Report: (To be filled by the concerned faculty / course coordinator)**

- CO1: Guest lecture will conduct on Boolean algebra , Boolean expressions and logic gates.
- CO2: Tutorial Classes will conduct on structural and behavioral modelling styles of HDL for simulating the combinational logic circuits.
- CO3: Extra practical experience will be given to the student on simulation and timing analysis of sequential circuits.
- CO4: Guest lecture will conduct on real time applications of flip flops.
- CO5: Guest lecture will conduct on finite state machine with mela and moore machines for sequence detector.
- CO6: Tutorial Classes will conduct on Hardware Description Language codes.

**Course Coordinator**

**Mentor**

**Head of the Department**  
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