



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Ms. B VEENA	Department:	Electronics and Communication Engineering
Regulation:	IARE - R20	Batch:	2022-2026
Course Name:	Digital System Design	Course Code:	AECC03
Semester:	III	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1 Outline binary arithmetic operations and optimize boolean functions using karnaugh and tabulation method.	2.30	2.20	2.3	Attained
CO2 Make use of basic logic gates to realize the combinational logic circuits used in conventional electronic circuits	1.60	2.20	1.7	Not Attained
CO3 Interpret the knowledge of flip-flops and latches in synchronous and asynchronous modules for memory storing applications.	2.30	2.20	2.3	Attained
CO4 Develop Mealy/Moore models and state diagrams for the complex sequential circuit applications.	0.70	2.20	1	Not Attained
CO5 Identify the different logic families, memories and programmable logic devices for understanding the architectural blocks of FPGA	1.40	2.20	1.6	Not Attained
CO6 Demonstrate the different modelling styles and data types in VHDL for implementing combinational and sequential circuits.	0.60	2.20	0.9	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO2: Guest lecture will be conduct on basic logic gates to realize the combinational logic circuits used in conventional electronic circuits


CO4: Tutorial classes will be conduct Mealy and Moore models and state diagrams for the complex sequential circuit applications.

CO5: Guest lecture will be conduct on different logic families, memories and programmable logic devices for understanding the architectural blocks of FPGA

CO6: Tutorial classes will be conduct different modelling styles and data types in VHDL for implementing combihational and sequential circuits.


Course Coordinator


Mentor


Head of the Department
Head of the Department
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