



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500043, Telangana

## ELECTRONICS AND COMMUNICATION ENGINEERING

### ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty: **Dr. B PRASHANTH** Department: **Electronics and Communication Engineering**  
Regulation: **IARE - UG20** Batch: **2022-2026**  
Course Name: **Computer Architecture** Course Code: **ACSC24**  
Semester: **VI** Target Value: **60% (1.8)**

#### Attainment of COs:

Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1 Illustrate the structure, characteristics of computer systems and the various functional units for understanding the components of computers	2.40	2.10	2.3	Attained
CO2 Demonstrate the computer languages, machine, symbolic and assembly levels for understanding execution of program	2.40	2.10	2.3	Attained
CO3 Make use of the number systems, their representations and conversion for the usage of instructions in digital computers	2.30	2.10	2.3	Attained
CO4 Summarize the register transfer language, represent memory and Arithmetic/ Logic/ Shift operations for implementation of micro operations	3.00	2.10	2.8	Attained
CO5 Identify the basics of hardwired and micro-programmed control of the CPU which generates the control signals to fetch and execute instructions	1.60	2.10	1.7	Not Attained
CO6 Compare different types of addressing modes for specifying the location of an operand	0.90	2.10	1.1	Not Attained

#### Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO5: Assignments will be given on basics of hardwired and micro-programmed control of the CPU.

CO6: Tutorial classes will be conduct on different types of addressing modes for specifying the location of an operand.

  
Course Coordinator

  
Mentor

  
Head of the Department

**Dr. P. MUNASWAMY** M.Tech, Ph.D, MISTE  
Professor & Head  
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