INSTITUTE OF AERONAUTICAL ENGINEERING
(Autonomous)
Dundigal, Hyderabad - 500043, Telangana

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	**Dr. V R SESHAGIRI RAO**	Department:	**Electronics and Communication Engineering**
Regulation:	**IARE - R20**	Batch:	**2020-2024**
Course Name:	**VLSI Design**	Course Code:	**AECC44**
Semester:	**VII**	Target Value:	**60% (1.8)**

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Summarize the MOSFET fabrication process , electrical properties and scaling for understanding latest developments in VLSI - Understand	2.30	2.20	2.3	Attained
CO2	Make use of stick diagrams and layout designs to convey layer information in MOSFET circuits-Apply	1.30	2.20	1.5	Not Attained
CO3	Analyze inverters, complex gates and dynamic CMOS circuits to calculate power consumption, distortion and speed of operation-Analyze	2.30	2.20	2.3	Attained
CO4	Illustrate data path subsystems and array subsystems using stick diagrams and layouts-Apply	2.30	2.20	2.3	Attained
CO5	Outline the role of Programmable logic devices for realization of complex boolean functions-Understand	2.30	2.20	2.3	Attained
CO6	Examine the test strategies, implementation approach on full custom and semi custom design for optimising speed, cost, reconfiguration and reliability parameters-Analyze	3.00	2.20	2.8	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO2: Guest lectures will be conduct on FET fabrication process and layout designs to convey layer information in MOSFET circuits

Course Coordinator **Mentor** **Head of the Department**

Dr. P. MUNASWAMY M.Tech, Ph.D, MISTE
Professor & Head
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