



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	J. Siva Ramakrishna	Department:	ECE
Regulation:	UG20	Batch:	2020-2024
Course Name:	IC Applications	Course Code:	AECCE12
Semester:	IV	Target Value:	60% (1.8)

Attainment of Cos:


Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Describe the principles and characteristics of op-amp circuits to perform arithmetic operations.	0.9	2.3	1.2	Attainment Target is not yet reached
CO2	Distinguish linear and non-linear applications of op-amp circuits to measure the output characteristics.	0.6	2.3	0.9	Attainment target is not yet reached
CO3	Design frequency selective circuits using OPAMP for audio and radio frequency ranges.	0.9	2.3	1.2	Attainment target is not yet reached
CO4	Demonstrate the characteristics, operation and applications of Multi-vibrators using IC555 timer.	0.9	2.3	1.2	Attainment target is not yet reached
CO5	Choose an appropriate A/D and D/A converter for signal processing applications.	0.9	2.3	1.2	Attainment Target is not yet reached
CO6	Analyze the characteristics of sequential and combinational digital integrated circuits for digital circuit design.	0.6	2.3	0.9	Attainment target is not yet reached

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Additional inputs will be provided on differential amplifiers, and characteristics and operation of op-amp circuits.
CO2: Additional inputs will be provided on linear and non-linear applications op-amps, such as integrators, differentiators, regulators.
CO3: Giving assignments and conducting tutorials on first order, second order low pass and high pass filters.
CO4: Giving assignments and conducting tutorials on Astable, monostable multivibrators.
CO5: Additional inputs will be provided on design of A/D and D/A converters such as, weighted resistor network type, R-2R type, SAR type, and flash type data converters.
CO6: Conducting Guest lectures on sequential and combinational logic circuits using ICs.


Course Coordinator


Mentor


HOD
Dr. P. Ashok Babu, M.E. Ph.D
Professor & Head
Electronics & Communication Engineering
Institute of Aeronautical Engineering
Dundigal, Hyderabad- 500 043. T.S.