



**ELECTRONICS AND COMMUNICATION ENGINEERING**  
**ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT**

Name of the faculty:	<b>Ms. P GANGA BHAVANI</b>	Department:	<b>Electronics and Communication Engineering</b>
Regulation:	<b>IARE - R20</b>	Batch:	<b>2020-2024</b>
Course Name:	<b>Computer Architecture</b>	Course Code:	<b>ACSC24</b>
Semester:	<b>VI</b>	Target Value:	<b>60% (1.8)</b>

**Attainment of COs:**

	<b>Course Outcome</b>	<b>Direct Attainment</b>	<b>Indirect Attainment</b>	<b>Overall Attainment</b>	<b>Observation</b>
CO1	Illustrate the structure, characteristics of computer systems and the various functional units for understanding the components of computers	3.00	2.00	2.8	Attained
CO2	Demonstrate the computer languages, machine, symbolic and assembly levels for understanding execution of program	2.30	2.00	2.2	Attained
CO3	Make use of the number systems, their representations and conversion for the usage of instructions in digital computers	1.60	2.00	1.7	Not Attained
CO4	Summarize the register transfer language, represent memory and Arithmetic/ Logic/ Shift operations for implementation of micro operations	1.40	2.00	1.5	Not Attained
CO5	Identify the basics of hardwired and micro-programmed control of the CPU which generates the control signals to fetch and execute instructions	3.00	2.00	2.8	Attained
CO6	Compare different types of addressing modes for specifying the location of an operand	2.10	2.00	2.1	Attained

**Action Taken Report: (To be filled by the concerned faculty / course coordinator)**

CO3: Guest lectures will be conduct on number systems, their representations and conversion for the usage of instructions in digital computers

CO4: Tutorial classes will be conduct on Arithmetic/ Logic/ Shift operations for implementation of micro operations

  
Course Coordinator

  
Mentor

  
Head of the Department