INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	**Ms. R Venkata Sravya**	Department:	**ECE**
Regulation:	**R18**	Batch:	**2019-2023**
Course Name:	**VLSI Design Laboratory**	Course Code:	**AECB29**
Semester:	**VII**	Target Value:	**60% (1.8)**

Attainment of COs:

Course Outcome		Overall Attainment	Observations
CO1	Make use of the static, dynamic and noise margin parameters of CMOS circuits for calculating figure of merit	3	Target Attained
CO2	Analyze complex gates, switch logic and transmission gates for performance optimization of distortion, power consumption and circuit delays.	3	Target Attained
CO3	Utilize 2 × 1 multiplexer building block and circuit symbols with necessary inter connections to build 4 × 1 multiplexer	3	Target Attained
CO4	Examine the conditions for optimum performance of lathes and registers with the knowledge of digital system design	3	Target Attained
CO5	Identify bandwidth, gain, and common mode rejection ratio parameters for casode amplifiers to protect amplifier from miller effect	3	Target Attained
CO6	Choose the stick diagrams, layouts using design rule checks (DRC) and verification for MOS circuits in various applications	3	Target Attained

Course Coordinator **Mentor** **HOD**

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