



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Ms. C Radhika	Department:	ECE
Regulation:	R18	Batch:	2019-2023
Course Name:	VLSI Design	Course Code:	AECB27
Semester:	VII	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Summarize the MOSFET fabrication process , electrical properties ,scaling for understanding latest trends in VLSI .	3	2	2.8	Target Attained
CO2	Make use of stick diagrams and layout designs to convey layer information in MOSFET circuits	1.6	2	1.7	Target not Attained
CO3	Analyze inverters, complex gates and dynamic CMOS circuits to calculate power consumption, distortion and speed of operation	2.3	2	2.2	Target Attained
CO4	Illustrate data path subsystems and array subsystems using stick diagrams and layouts.	0	2	0.4	Target not Attained
CO5	Outline the role of Programmable logic devices for realization of complex boolean functions.	0	2	0.4	Target not Attained
CO6	Examine the test strategies, implementation approach on full custom and semi custom design for optimizing speed, cost, reconfiguration and reliability parameters.	2.1	2	2.1	Target Attained

Action Taken Report: (To be filled by the concerned faculty/course coordinator)

CO2: Guest lectures will be conducted on use of stick diagrams and layout designs to convey layer information in MOSFET circuits

CO4: Assignments will be provided on data path subsystems and array subsystems using stick diagrams and layouts.

CO5: Additional information will be provided on the role of Programmable logic devices for realization of complex boolean functions

C Radhika
Course Coordinator

C Radhika
Mentor

P. Munaswamy
HOD

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