INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	**K Chaithanya**	Department:	**ECE**
Regulation:	**IARE-R18**	Batch:	**2019-2023**
Course Name:	**Digital Signal Processing**	Course Code:	**AECB23**
Semester:	**VI**	Target Value:	**60% (1.8)**

Attainment of Cos:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Illustrate the concept of discrete time signals and systems for analysing the response of LTI system in time domain and frequency domain.	1.6	2.2	1.7	Target Not Attained
CO2	Construct the Decimation-in-time fast fourier transform and decimation-in-frequency fast fourier transform for reducing computational complexity of DFT.	1.6	2.2	1.7	Target Not Attained
CO3	Implement the digital filters and their realization structures using various transformation technique.	0.9	2.1	1.1	Target Not Attained
CO4	Analyze the performance characteristics of digital filters to meet expected system specifications using MATLAB.	0	2.1	0.4	Target Not Attained
CO5	Interpret the efficient implementation of sample rate conversion of digital signals to interface the digital systems with different sampling rates.	0.9	2.1	1.1	Target Not Attained
CO6	Identify the errors in analog to digital conversion for tolerating finite word length effects.	0.9	2.2	1.2	Target Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Conducting Guest lectures on LTI system in time domain and frequency domain for more practice

CO2: Practice tests are conducted on frequency fast Fourier transform for reducing computational complexity of DFT for more practice.

CO3: Giving assignments and conducting tutorials on digital filters and their realization structures for more practice

CO4: Practice tests are conducted on the performance characteristics of digital filters for more practice

CO5: Giving assignments and conducting tutorials on efficient implementation of sample rate conversion of digital signals to interface the digital systems for more practice

CO6: Additional inputs will be provided on analog to digital conversion for tolerating finite word length effects for more practice

Course Coordinator

Mentor

HOD

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