



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Dr. V Vijay	Department:	ECE
Regulation:	R18	Batch:	2019-2023
Course Name:	Digital System Design	Course Code:	AECB07
Semester:	III	Target Value:	60% (1.8)

Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Outline binary arithmetic operations and optimize boolean functions using karnaugh and tabulation method.	2.3	2.1	2.3	Attainment target reached
CO2	Apply combinational circuits for realization of basic building blocks of conventional electronic circuits.	3	2.1	2.8	Attainment target reached
CO3	Interpret the knowledge of flip-flops and latches in synchronous and asynchronous modules for memory storing applications.	3	2.1	2.8	Attainment target reached
CO4	Extend the logic design techniques for ECL, TTL and CMOS methodologies for designing the fundamental gate level modelling.	3	2.2	2.8	Attainment target reached
CO5	Extend the characteristics of logic families and PLDs to enhance the design skills indigital integrated circuits.	3	2.2	2.8	Attainment target reached
CO6	Evaluate synthesis and simulation of VHDL modules for implementing combinational and sequential circuits.	3	2.2	2.4	Attainment target reached


Course Coordinator


Mentor


HOD

Head of the Department
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