



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Mr. B. Naresh	Department:	ECE
Regulation:	R18	Branch:	2019-2023
Course Name:	Electronic Devices and Circuits	Course Code:	AECB06
Semester:	III	Target Value:	60% (1.8)

Attainment of COs:


Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Summarize the semiconductor device properties using energy band diagrams	1.6	2.2	1.7	Attainment target is not yet reached
CO2	Illustrate the volt-ampere characteristics of pn junction diode for finding cut-in voltage, static and dynamic resistances.	1.6	2.2	1.7	Attainment target is not yet reached
CO3	Apply the pn junction characteristics for the diode applications such as switch and rectifiers.	0.9	2.2	1.2	Attainment target is not yet reached
CO4	Demonstrate the constructional features and principle operation of bipolar and uni-polar devices for distinguishing between cut off, active and saturation regions of operation	0.9	2.1	1.1	Attainment target is not yet reached
CO5	Establish the relations between current gain, voltage gain of bipolar junction transistor and field effect transistor respectively using their characteristics	0.9	2.2	1.2	Attainment target is not yet reached
CO6	Analyse the input and output characteristics of transistor configurations for determining the input - output resistances, current gain and voltage gain	0.9		0.7	Attainment target is not yet reached

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Conducting Guest lectures on the semiconductor device properties for better understanding.
CO2: Giving assignments and conducting tutorials on the volt-ampere characteristics of pn junction diode for more practice.
CO 3: Additional inputs will be provided on the pn junction characteristics for the diode applications for improving students performance.
CO 4: Additional inputs and assignments will be provided on constructional features and principle operation of bipolar and uni-polar devices.
CO 5: Additional inputs will be provided on current gain, voltage gain of bipolar junction transistor and field effect transistor for improving students performance.
CO 6: Giving assignments and conducting tutorials on the input and output characteristics of transistor configurations for determining the input - output resistances, current gain and voltage gain


Course Coordinator


Mentor


HOD
Head of the Department
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