INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	**Ms. K S Indrani**	Department:	**ECE**
Regulation:	**IARE-R18**	Branch:	**2018-2022**
Course Name:	**VLSI Design**	Course Code:	**AECB27**
Semester:	**VII**	Target Value:	**60% (1.8)**

Attainment of Cos:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Summarize the MOSFET fabrication process, electrical properties, scaling for analyzing reliability issues and understanding latest trends in VLSI .	2.3	2.4	2.3	Attainment target is reached
CO2	Develop the stick diagrams, layouts of MOS circuits using lambda, absolute and Euler physical design rules	2.3	2.3	2.3	Attainment target is reached
CO3	Describe inverters, complex gates and dynamic CMOS circuits for power consumption, distortion and speed of operation.	2.3	2.4	2.3	Attainment target is reached
CO4	Explain data path subsystems containing arithmetic logic units, parity generators, comparators and memories using stick diagrams and layouts..	1.6	2.4	1.8	Attainment target is reached
CO5	Outline the role of Programmable logic devices for realization of complex boolean functions	2.3	2.4	2.3	Attainment target is reached
CO6	Examine the test strategies, implementation approach on full custom and semi custom design for speed, cost, reconfiguration and reliability parameters.	1.6	2.4	1.8	Attainment target is reached

Course Coordinator

Mentor

HOD

Head of the Department
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