



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT


Name of the Faculty:	Dr. V Vijay	Department:	ECE
Regulation:	IARE-R16	Branch:	2017-2021
Course Name:	VLSI Design Laboratory	Course Code:	AEC112
Semester:	VII	Target Value:	60% (1.8)

Attainment of Cos:

Course Outcome		Overall Attainment	Observations
CO1	Calculate the static, dynamic and noise margin parameters of CMOS inverter using the output and transfer characteristics of MOSFETs.	2.3	Attainment target reached
CO2	Analyze complex gates, switch logic and transmission gates for performance optimization of distortion, power consumption and circuit delays.	2.3	Attainment target reached
CO3	Build 4 X 1 multiplexer and ring oscillator using multiplexer	2.3	Attainment target reached
CO4	Examine the conditions for optimum performance of latches and registers with the knowledge of digital system design.	2.3	Attainment target reached
CO5	Calculate bandwidth, gain, and common mode rejection ratio parameters for differential, MOSFET and cascode amplifiers.	2.3	Attainment target reached
CO6	Build the stick diagrams, layouts of MOS circuits using design rule checks (DRC) and verification.	2.3	Attainment target reached


Course Coordinator


Mentor


HOD
Head of the Department
Electronics and Communication Engineering
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