INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Mr.V R Seshagiri Rao	Department:	ECE
Regulation:	IARE-R16	Branch:	2017-2021
Course Name:	VLSI design	Course Code:	AEC017
Semester:	VII	Target Value:	60% (1.8)

Attainment of Cos:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Outline the effects of scaling on MOS circuits for area, speed, power and fabrication flow with constant voltage or field or combined modelling	3	2.4	2.9	Attainment target reached
CO2	Describe inverters, complex gates and dynamic CMOS circuits for power consumption and speed of operation.	2.3	2.4	2.3	Attainment target reached
CO3	Build the stick diagrams, layouts of MOS circuits using lambda, absolute and Euler physical design rules.	1.6	2.4	1.8	Attainment target reached
CO4	Distinguish the structure, implementation approaches on full custom and semicustom design for speed, cost, reconfiguration and reliability parameters.	1.6	2.4	1.8	Attainment target reached
CO5	Explain data path subsystems containing arithmetic logic units, parity generators, comparators and memories using stick diagrams and layouts.	3	2.4	2.9	Attainment target reached
CO6	Construct simulation, synthesis and design verification of logic circuits using the key elements of VLSI design flow.	0	2.4	0.5	Attainment target is not yet reached

Action Taken Report: (To be filled by the concerned faculty / course coordinator)
CO6: Giving assignments and conducting tutorials on implementation approaches on full custom and semicustom design for speed, cost and reconfiguration for saving memory for more practice.

Course Coordinator **Mentor** **HOD**

Head of the Department
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