



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)  
Dundigal, Hyderabad - 500 043

## ELECTRONICS AND COMMUNICATION ENGINEERING

### ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Mr. S Lakshmanachari	Department:	ECE
Regulation:	IARE-R16	Branch:	2017-2021
Course Name:	Pulse and Digital Circuits	Course Code:	AEC006
Semester:	IV	Target Value:	60% (1.8)

#### Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Analyze the response of linear and non-linear wave shaping circuits for impulse and pulse inputs with different time constants.	0	2.1	0.4	Attainment target is not reached
CO2	Build bistable, monostable and astable multivibrator circuits using transistors for real time applications.	0.6	2.1	0.9	Attainment target is not reached
CO3	Apply the operating principles of diodes and transistors for the designing of sampling gates.	0.9	2.1	1.1	Attainment target is not yet reached
CO4	Illustrate different methods to generate time base waveforms using Bootstrap and Miller circuits.	0.7	2.1	1	Attainment target is not yet reached
CO5	Understand the synchronization and frequency division concepts using relaxation devices and sweep circuits.	0.9	2.1	1.1	Attainment target is not reached
CO6	Summarize the characteristics of digital logic families for designing of digital logic circuits.	0.9	2.1	1.1	Attainment target is not reached

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO 1: Additional inputs will be provided on linear and non-linear wave shaping circuits.

CO 2: Conducting Guest lectures on triggering of multivibrators like bi-stable and mono stable.

CO 3: Conducting Guest lectures on time base generators and sampling gates.

CO 4: Giving assignments and conducting tutorials on synchronization of a sweep circuit with symmetrical signals.

CO 5: Additional inputs will be provided on Schmitt Trigger Circuit to determine the pulses correctly.

CO 6: Additional inputs will be provided on multivibrator circuits using transistors for real time applications and digital logic families

  
Course Coordinator

  
Mentor

  
HOD

Head of the Department  
Electronics and Communication Engineering  
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