



# INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad - 500 043

## ELECTRONICS AND COMMUNICATION ENGINEERING

### ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	B. Naresh	Department:	ECE
Regulation:	IARE-R16	Branch:	2017-2021
Course Name:	Electronic Devices and Circuits	Course Code:	AEC001
Semester:	II	Target Value:	60% (1.8)

#### Attainment of COs:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Illustrate the characteristics of semiconductor devices for determining the device parameters such as resistances, current gain and voltage gain.	1	2.2	1.2	Attainment target is not reached
CO2	Apply the pn junction characteristics for the diode applications such as switch and rectifiers.	1	2.1	1.2	Attainment target is not reached
CO3	Examine DC and AC load line analysis of BJT and FET amplifiers for optimal operating level regardless of input, load placed on the device.	1.3	2.2	1.5	Attainment target is not yet reached
CO4	Extend the biasing techniques for bipolar and uni-polar transistor for establishing a proper operating point.	1	2.1	1.2	Attainment target is not yet reached
CO5	Utilize low frequency model for estimation of the characteristic parameters of BJT, FET amplifier circuits.	0.3	2.2	0.7	Attainment target is not reached
CO6	Demonstrate the working principle of special purpose semiconductor diodes and transistors for triggering and voltage regulation applications.	0.3	2.1	0.7	Attainment target is not reached

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

CO1: Conducting Guest lectures on break down mechanisms in semiconductor diodes.

CO 2 : Additional inputs will be provided on half wave and full wave rectifiers for improving students performance.

CO 3: Additional inputs will be provided on Bipolar Junction Transistors, Field Effect Transistors and MOSFET construction and operation.

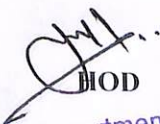
CO 4: Additional inputs will be provided on biasing and bias compensation techniques for improving students performance.

CO 5: Giving assignments and conducting tutorials on analysis determination of h-parameters from transistor characteristics

CO 6: Giving assignments and conducting tutorials on special purpose semiconductor diodes and transistors for triggering and voltage regulation applications

  
Course Coordinator

  
Mentor

  
Head of the Department  
Electronics and Communication Engineering  
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