INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)
Dundigal, Hyderabad - 500 043

ELECTRONICS AND COMMUNICATION ENGINEERING

ATTAINMENT OF COURSE OUTCOME- ACTION TAKEN REPORT

Name of the Faculty:	Ms.S Swathi	Department:	ECE
Regulation:	IARE-R16	Batch:	2016-2020
Course Name:	Digital IC Applications using VHDL	Course Code:	AEC516
Semester:	V Semester	Target Value:	60% (1.8)

Attainment of Cos:

Course Outcome		Direct Attainment	Indirect Attainment	Overall Attainment	Observations
CO1	Distinguish the types of logic families, their static and dynamic characteristics for providing the interface between CMOS, TTL and ECL logic.	3.00	2.40	2.9	Attained
CO2	Describe the basic language elements, data flow, behavioral and structural modelling constructs of VHDL used for the simulation and synthesis of digital circuits.	2.30	2.50	2.3	Attained
CO3	Make use of language constructs in three modelling styles to implement the VHDL code for combinational ICs (Apply)	2.70	2.50	2.7	Attained
CO4	Implement efficient techniques at circuit level for improving power and speed of combinational circuits like encoders, comparators and barrel shifter(Apply)	2.30	2.50	2.3	Attained
CO5	Choose the latches and flip flops to develop the VHDL model for synchronous and asynchronous sequential circuits	2.30	2.50	2.3	Attained
CO6	Identify the types of memories with efficient architectures to improve access times, power consumption.	3.00	2.40	2.9	Attained

Course Coordinator **Mentor** **HOD**

Head of the Department
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