INSTITUTE OF AERONAUTICAL ENGINEERING
(Autonomous)
Dundigal, Hyderabad - 500043, Telangana

COMPUTER SCIENCE AND ENGINEERING
ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	**Ms. Y MEGHAMALA**	Department:	**Computer Science and Engineering**
Regulation:	**IARE - R20**	Batch: .	**2022-2026**
Course Name:	**Analog and Digital Electronics**	Course Code:	**AECC08**
Semester:	**III**	Target Value:	**60% (1.8)**

Attainment of COs:

	Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Demonstrate the volt-ampere characteristics of semiconductor devices for finding cut-in voltage, resistance and capacitance.	0.90	2.10	1.1	Not Attained
CO2	Illustrate half wave and full wave rectifier circuits with filter and without filters used to convert the alternating current in to direct current.	1.60	2.10	1.7	Not Attained
CO3	Analyze the input and output characteristics of transistor configurations and small signal h-parameter models to determine the input - output resistances, current gain and voltage gain	1.60	2.10	1.7	Not Attained
CO4	Identify the functionality of logic gates, parity code and hamming code techniques for error detection and correction of single bit in digital systems.	3.00	2.10	2.8	Attained
CO5	Make use of appropriate logic gates to implement combinational logic circuits.	2.30	2.10	2.3	Attained
CO6	Select a required flip flop to realize synchronous and asynchronous counters for memory storing applications.	2.30	2.10	2.3	Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)
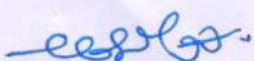
CO1: There is a need to give the oral presentations of pre requisites subjects on particular topic pertaining to the semiconductors and working principles with necessary diagrams.

CO2: Need to conduct seminars on electronic devices to get more knowledge on how they work with respect to filters and rectifiers and as well conversion of currents from AC to DC.

CO3: students should be given maximum analytical assignments on transistors, small signal h parameters and specifications.

Course Coordinator   **Mentor**

Head of the Department
Head of the Department
Computer Science and Engineering
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