INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad -500 043

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

ATTAINMENT OF COURSE OUTCOMES (COS) – ACTION PLAN

Name of the Faculty	Ms . Sreevani	Department	CSE
Regulations	UG20	Batch	2020-2024
Course Name	Analog and Digital Electronics	Course Code	AECC08
Semester	III	Target Value	70% (2.1 on 3 Scale)

Attainment of COs:

Course Outcomes		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Demonstrate the volt-ampere characteristics of semiconductor devices for finding cut-in voltage, resistance and capacitance.	0.9	2.4	1.2	Target not attained
CO2	Illustrate half wave and full wave rectifier circuits with filter and without filters used to convert the alternating current in to direct current.	0.9	2.3	1.2	Target not attained
CO3	Analyze the input and output characteristics of transistor configurations and small signal h-parameter models to determine the input - output resistances, current gain and voltage gain	0.9	2.3	1.2	Target not attained
CO4	Identify the functionality of logic gates, parity code and hamming code techniques for error detection and correction of single bit in digital systems.	0.9	2.3	1.2	Target not attained
CO5	Make use of appropriate logic gates to implement combinational logic circuits.	0.9	2.4	1.2	Target not attained
CO6	Select a required flip flop to realize synchronous and asynchronous counters for memory storing applications.	0.9	2.4	1.2	Target not attained

Action taken report:

CO1,CO2,CO3: Additional classes were conducted on fundamentals of digital electronics to create interest in basics of computer hardware

CO4,CO5,CO6: ELRV videos were given to students to understand the need of digital electronics for computer science

Course Coordinator

HOD , CSE

Head of the Department
Computer Science and Engineering
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