



INSTITUTE OF AERONAUTICAL ENGINEERING
(Autonomous)
Dundigal, Hyderabad - 500043, Telangana

COMPUTER SCIENCE AND ENGINEERING

ATTAINMENT OF COURSE OUTCOME - ACTION TAKEN REPORT

Name of the faculty:	Dr. V CHANDRA JAGAN MOHAN	Department:	Computer Science and Engineering
Regulation:	IARE - R18	Batch:	2018-2022
Course Name:	Fundamental of Electrical Engineering	Course Code:	AEEB01
Semester:	I	Target Value:	70% (2.1)

Attainment of COs:

Course Outcome	Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1 Know the fundamental concepts of electric circuits for computing voltage and current relationship of passive elements.	0.00	2.40	0.5	Not Attained
CO2 Solve complex electrical circuits by applying network reduction techniques for reducing into a simplified circuit.	3.00	2.50	2.9	Attained
CO3 Make use of various network theorems for simplifying complex electrical networks.	3.00	2.50	2.9	Attained
CO4 Define basic nomenclature of single phase AC circuits for obtaining impedance, admittance of series and parallel circuits.	0.90	2.50	1.2	Not Attained
CO5 Interpret the power factor in single phase circuits with various combination of network elements for computing active and reactive power.	2.00	2.50	2.1	Attained
CO6 Explain formation of incident, cut-set and tie set matrices using which characteristics of electrical circuits can be studied.	0.90	0.00	0.7	Not Attained

Action Taken Report: (To be filled by the concerned faculty / course coordinator)

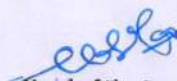
CO1: A Demonstration in Lab will be arranged to make student strong in fundamentals of Electric circuits.

CO4: A seminar by an expert from industry will be arranged to train students in usage of series and parallel circuits

CO6: A case study on electrical circuit characteristics will be discussed to enhance students interest in the same topic


Course Coordinator


Mentor


Head of the Department
Computer Science and Engineering
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