



INSTITUTE OF AERONAUTICAL ENGINEERING

(Autonomous)

Dundigal, Hyderabad -500 043

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

ATTAINMENT OF COURSE OUTCOMES (COS) – ACTION PLAN

Name of the Faculty	Mr. Mohammad khadir	Department	CSE
Regulations	R16	Batch	2016 -2020
Course Name	Microprocessor & Interfacing	Course Code	AEC021
Semester	V	Target Value	70% (2.1 on 3 Scale)

Attainment of COs:

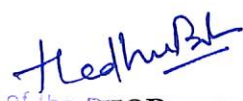
Course Outcomes		Direct Attainment	Indirect Attainment	Overall Attainment	Observation
CO1	Outline the functional components of microprocessors for understanding the operation of architectures.	3	2.5	2.9	Target attained
CO2	Make use of addressing modes and instruction set of target microprocessors for writing an assembly language programs to perform a task.	2.3	2.5	2.3	Target attained
CO3	Demonstrate the internal architecture and modes of operation of peripheral devices for interfacing memory and I/O devices.	1.6	2.5	1.8	Target not attained
CO4	Illustrate the interrupt handling mechanism in microprocessors and microcontrollers using interrupt controller.	1.6	2.5	1.8	Target not attained
CO5	Choose an appropriate data transfer scheme and hardware for data transfer between the devices.	3	2.5	2.9	Target attained
CO6	Develop microprocessor-based applications using necessary input and output devices.	0	2.5	0.5	Target not attained

Action taken report:

CO3, CO6: More programming exercises on assembly language to demonstrate microprocessor architectures.
CO4: Make students to develop a complete microcontroller-based application to enhance understanding of interrupt handling mechanism.


Course Coordinator


Mentor


Head of the Department
Computer Science and Engineering
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