

## DIGITAL COMMUNICATIONS LABORATORY

**V Semester: ECE**

Course Code	Category	Hours / Week			Credits	Maximum Marks		
AECB22	Core	L	T	P	C	CIA	SEE	Total
		-	-	2	1	30	70	100
<b>Contact Classes: Nil</b>	<b>Tutorial Classes: Nil</b>	<b>Practical Classes: 24</b>			<b>Total Classes: 24</b>			

### I. COURSE OVERVIEW:

This lab course gives the hands on experience in elements of digital communication systems. The design of various coding techniques, pulse analog and digital modulations to analyse signal to noise ratio, bit error rate, power and bandwidth for digital communication systems. This lab is useful in the digital signal processors in secured communication systems, multimedia communications and data storage applications.

### II. OBJECTIVES:

**The course should enable the students to:**

- I The Elements of digital communication systems to convert continuous time signals into discrete time signals.
- II The pulse analog modulation techniques, generation and detection of digital modulation techniques.
- III The time and frequency domain analysis of the signals in communication system by using MATLAB tools.

### III. COURSE OUTCOMES:

**After successful completion of the course, students should be able to:**

- CO 1 **Examine** sampling theorem for processing of different signals such as low pass signals, band-limited signals and band pass signals. . Analyze
- CO 2 **Classify** the pulse modulation and demodulation methods for encoded data in analog to digital conversion. Analyze
- CO 3 **Apply** the concept of pulse code modulation and demodulation for the equivalent sequence of binary code word data. Analyze
- CO 4 **Categorize** the digital modulation techniques used for transfer a digital bit stream over an analog channel at a high frequency. Analyze
- CO 5 **Determine** bit rate in delta modulation and demodulation process for the no. of bits per sample are transmitted. Apply
- CO 6 **Develop** frequency domain description of different digital modulation techniques for spectral characteristics analysis. Apply

### LIST OF EXPERIMENTS

<b>Week-1</b>	<b>SAMPLING THEOREM – VERIFICATION</b>
Verification of sampling theorem for under, perfect, over sampling cases	
<b>Week-2</b>	<b>PULSE AMPLITUDE MODULATION AND DEMODULATION</b>
Generation of Pulse Amplitude modulation and demodulation using hardware and matlab	
<b>Week-3</b>	<b>PULSE WIDTH MODULATION AND DEMODULATION</b>
Generation of Pulse width modulation and demodulation using hardware and matlab	

<b>Week-4</b>	<b>PULSE POSITION MODULATION AND DEMODULATION.</b>
Generation of pulse position modulation and demodulation using hardware and matlab	
<b>Week-5</b>	<b>PULSE CODE MODULATION</b>
Generation of pulse code modulation and demodulation using hardware and understanding the concept analog to digital conversion	
<b>Week-6</b>	<b>DIFFERENTIAL PULSE CODE MODULATION</b>
Generation of differential pulse code modulation and demodulation using hardware	
<b>Week-7</b>	<b>DELTA MODULATION.</b>
Generation of delta modulation and demodulation using hardware .Understanding difference between PCM and DM	
<b>Week-8</b>	<b>FREQUENCY SHIFT KEYING</b>
Generation of Frequency shift keying modulation and demodulation using hardware	
<b>Week-9</b>	<b>PHASE SHIFT KEYING.</b>
Generation of Phase shift keying modulation and demodulation using hardware	
<b>Week-10</b>	<b>DIFFERENTIAL PHASE SHIFT KEYING</b>
Generation of Differential Phase shift keying modulation and demodulation using hardware	
<b>Week-11</b>	<b>AMPLITUDE SHIFT KEY(ASK)</b>
Generation of Amplitude Shift Key modulation and demodulation using hardware	
<b>Week-12</b>	<b>QUADRATURE PHASE SHIFT KEYING</b>
Generation of QPSK modulation and demodulation using hardware	
<b>Week-13</b>	<b>MATLAB for QPSK &amp; SIMULINK for DPSK.</b>
Understand frequency domain description of Quadrature Phase Shift Keying and Differential Phase shift keying	
<b>Week-14</b>	<b>STUDY OF THE SPECTRAL CHARACTERISTICS OF AMPLITUDE MODULATION</b>
Understand frequency domain description of Amplitude Modulation	
<b>Reference Books:</b>	
<ol style="list-style-type: none"> <li>1. K.SamShanmugam, “Digital and Analog Communication Systems”, Joh Wiley &amp; Sons, 2<sup>nd</sup> Edition, 2005.</li> <li>2. B.P.Lathi, “Modern Analog and Digital Communication”, Oxford reprint, 3<sup>rd</sup> Edition, 2004.</li> <li>3. Singh,Sapre, “Communication Systems Analog and Digital”, TMH, 2<sup>nd</sup> Edition,2004</li> </ol>	
<b>Web References:</b>	
<ol style="list-style-type: none"> <li>1. <a href="https://ocw.mit.edu/courses/electrical.../6...digital-communications.../lecture-notes">https://ocw.mit.edu/courses/electrical.../6...digital-communications.../lecture-notes</a></li> <li>2. <a href="https://everythingvtu.wordpress.com">https://everythingvtu.wordpress.com</a></li> <li>3. <a href="http://www.iare.ac.in">http://www.iare.ac.in</a></li> </ol>	

**SOFTWARE AND HARDWARE REQUIRED FOR A BATCH OF 36 STUDENTS**

**HARDWARE:** Desktop Computer Systems 18 nos

**SOFTWARE:**MATLAB